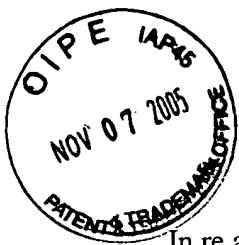


JPW



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q79066

Hisashi ISHIKAWA

Appln. No.: 10/743,919

Group Art Unit: 2824

Confirmation No.: 9181

Examiner: Jung H. HUR

Filed: December 24, 2003

For: SEMICONDUCTOR MEMORY DEVICE AND MOUNT-TYPE SEMICONDUCTOR
DEVICE

INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. §§ 1.97 and 1.98

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure under 37 C.F.R. § 1.56, Applicant hereby notifies the U.S. Patent and Trademark Office of the documents which are listed on the attached PTO/SB/08 A & B (modified) form and listed herein and which the Examiner may deem material to patentability of the claims of the above-identified application.

1. Japanese Unexamined Patent Application Publication No. 2002-007200, published January 11, 2002 (with English abstract), previously submitted in an Information Disclosure Statement dated August 17, 2005.
2. Japanese Unexamined Patent Application Publication No. 2002-230963, published August 16, 2002 (with English abstract), previously submitted in an Information Disclosure Statement dated August 17, 2005.
3. "Usage of DDR-SDRAM," Elpida Memory Inc., January 2002, pp. 37-39, 40-47, 52-59, 60-64, 75, and 76, previously submitted in an Information Disclosure Statement dated August 17, 2005.

INFORMATION DISCLOSURE STATEMENT

U.S. Appln. No.: 10/743,919

Atty. Docket No.: Q79066

4. "TC59LM814/06BFT-22, -24, and -30 data sheet," Toshiba Corporation, March 19, 2001, pp. 7, 8, 12, 13, 27, 28, and 31.
5. Japanese Unexamined Patent Application Publication No. 05-346886, published December 17, 1993 (with English abstract), previously submitted in an Information Disclosure Statement dated August 17, 2005.
6. Japanese Unexamined Patent Application Publication No. 08-241245, published September 17, 1996 (with English abstract), previously submitted in an Information Disclosure Statement dated August 17, 2005.
7. Japanese Unexamined Patent Application Publication No. 2002-082829, published March 22, 2002, (with English abstract), previously submitted in an Information Disclosure Statement dated August 17, 2005.
8. Kazuteru KASAHARA: VIA Technologies DDR333 Summit Report: Full-scale adoption of DDR333 slated for 2003," (online), February 22, 2002, Impress Co., Ltd., (retrieved August 1, 2005), Internet <URL: <http://pc.watch.impress.co.jp/docs/2002/0222/via2.htm>>.

One copy of each of the listed documents is submitted herewith, except for those indicated as being previously submitted.

The present Information Disclosure Statement is being filed after the later of three months from the application's filing date and the mailing date of the first Office Action on the merits, but before a Final Office Action, Notice of Allowance, or an action that otherwise closes prosecution in the application (whichever is earlier), and therefore Applicant is filing concurrently herewith a Statement Under 37 C.F.R. § 1.97(e). No fee under 37 C.F.R. § 1.17(p) is required.

In compliance with the concise explanation requirement under 37 C.F.R. § 1.98(a)(3) for foreign language documents, Applicant encloses herewith a copy of a corresponding Japanese Office Action, dated August 9, 2005, and an English translation of the pertinent portions thereof which cites such documents and indicates the degree for relevance found by the foreign office.

INFORMATION DISCLOSURE STATEMENT

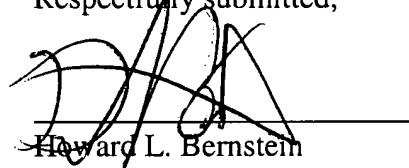
U.S. Appln. No.: 10/743,919

Atty. Docket No.: Q79066

The submission of the listed documents is not intended as an admission that any such document constitutes prior art against the claims of the present application. Applicant does not waive any right to take any action that would be appropriate to antedate or otherwise remove any listed document as a competent reference against the claims of the present application.

The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account. A duplicate copy of this paper is attached.

Respectfully submitted,


Howard L. Bernstein
Registration No. 25,665

SUGHRUE MION, PLLC
Telephone: (202) 293-7060
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WASHINGTON OFFICE

23373

CUSTOMER NUMBER

Date: November 7, 2005

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete if Known

Application Number	10/743,919
Confirmation Number	9181
Filing Date	December 24, 2003
First Named Inventor	Hisashi ISHIKAWA
Art Unit	2824
Examiner Name	Jung H. HUR
Attorney Docket Number	Q79066

Sheet	1	of	1
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Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published.	Translation ⁶
		"TC59LM814/06BFT-22, -24, and -30 data sheet," Toshiba Corporation, March 19, 2001, pp. 7, 8, 12, 13, 27, 28, and 31.	
		Kazuteru KASAHARA: VIA Technologies DDR333 Summit Report: Full-scale adoption of DDR333 slated for 2003," (online), February 22, 2002, Impress Co., Ltd., (retrieved August 1, 2005), Internet <URL: http://pc.watch.impress.co.jp/docs/2002/0222/via2.htm >.	

Examiner Signature		Date Considered	
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¹Applicant's unique citation designation number (optional). ²See Kind Codes of USPTO Patent Documents at www.uspto.gov, MPEP 901.04 or in the comment box of this document. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST. 3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to indicate here if English language Translation is attached.



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DEVICE

STATEMENT UNDER 37 C.F.R. § 1.97(e)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The undersigned hereby states, upon information and belief:

That each item of information contained in the Information Disclosure Statement filed concurrently herewith was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of said Information Disclosure Statement.

Respectfully submitted,

Howard L. Bernstein
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